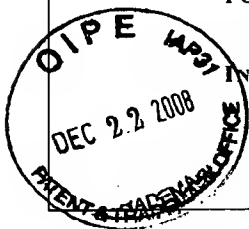


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PATENT AND TRADEMARK OFFICE  
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Sheet 1 of 1

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Toshiro AKINO

FILING DATE

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September 29, 2005

2816

## U.S. PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
1.		US 5,543,650	Aug. 6, 1996	Au et al.		
2.		US 6,147,508	Nov. 14, 2000	Beck et al.		
3.		US 6,232,793	May 15, 2001	Arimoto et al.		
4.		US 6,249,027	June 19, 2001	Burr		

## FOREIGN PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translation Yes	No
5.		EP 1 126 523	Aug. 22, 2001	Europe			
6.		JP 9069767	March 11, 1997	Japan		Abstract	

## OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)

Ref. Desig.	Examiner's Initials	
7.		Jeffrey D. Morse and David H. Navon, "Optimized Design of a Merged Bipolar MOSFET Device", November 1985, Vol ED-32. No. 11.
8.		European Search Report dated November 11, 2008 for corresponding application No. 04719623.3-1235/1617477.

DJJ/AZP/akp

Examiner: /Colleen O'Toole/

Date Considered: 05/02/2009

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /CO/